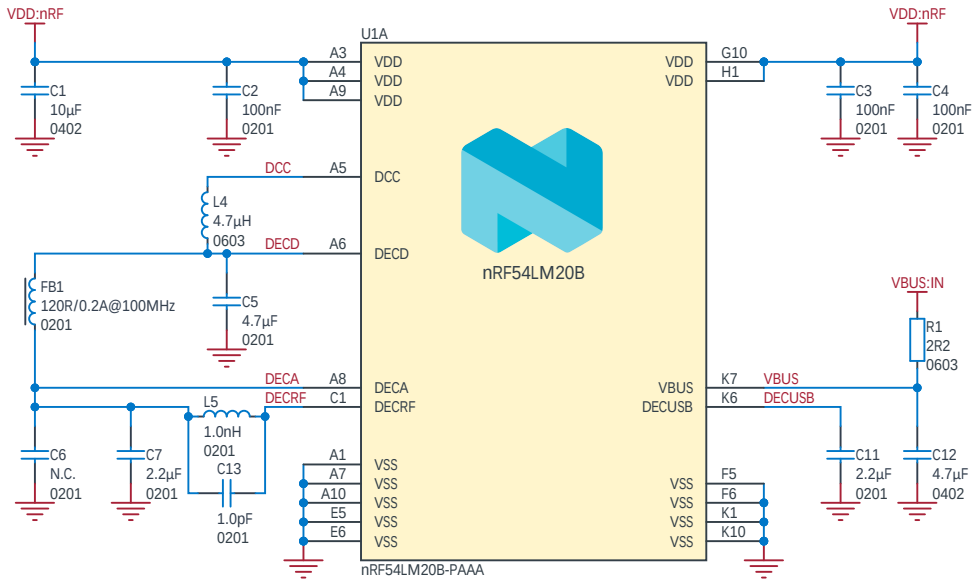
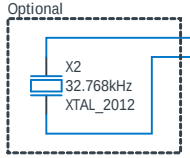
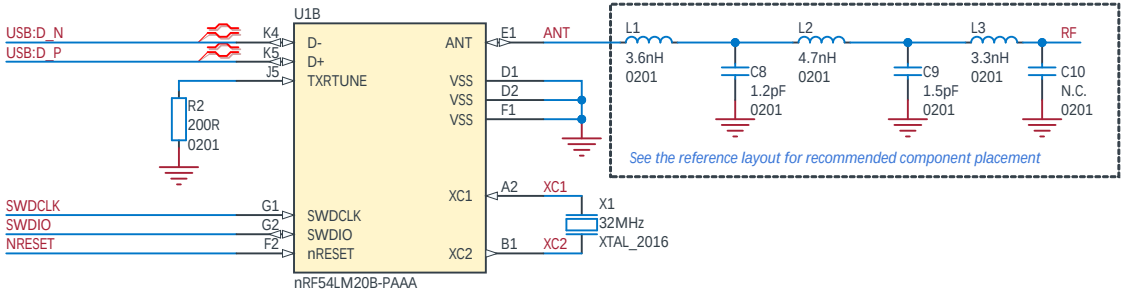
Reference Schematic for nRF54LM20B-PAAA

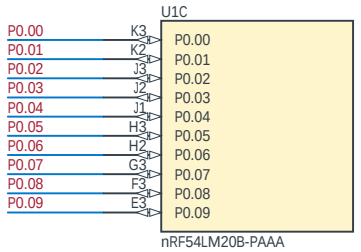
Config 1: VDD:nRF supplied by external supply, Enabled DC/DC

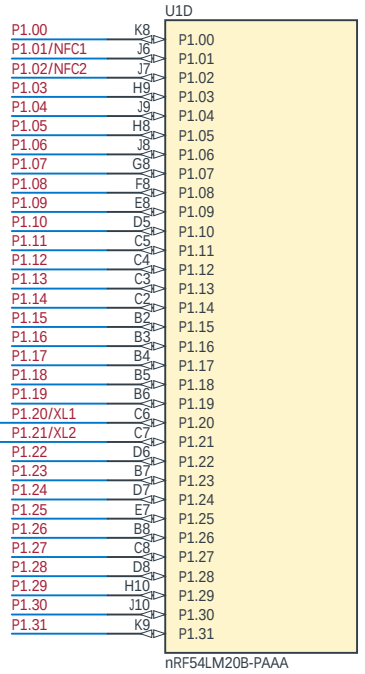
Reference schematic revisions earlier than 1.0 can be changed without prior notice.
Always refer to Nordic Semiconductor documentation for the latest information on recommended reference layout files before starting your design.

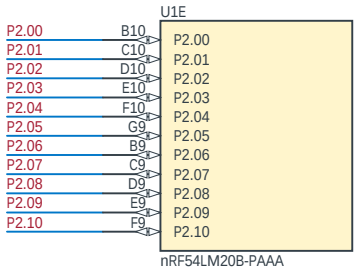


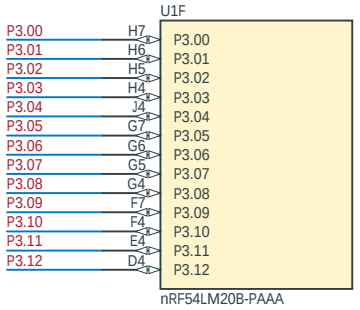












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Title			
nRF54LM20B-PAAA Reference Schematic, 8 layer PCB			
Size	Config. No.	Device	Revision
A4	1	nRF54LM20B-PAAA	1.0
Date: 2026-11-17			Sheet 1 of 1
File: nRF54LM20B-PAAA_Reference_Schematic_8L_config1.SchDoc			
Classification: Public			